

PATENT NUMBER

09/802120
03/08/01

ISSUE CLASSIFICATION	
Class	Subclass

O.I.P.E. SCANNED <u>AK</u> <u>EW</u> Q.A.	PATENT DATE
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APPLICATION NO. 09/802120	CONT/PRIOR D	CLASS 712	SUBCLASS 24	ART UNIT 2183	EXAMINER <u>Chan Eddie</u>
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APPLICANTS

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III

VLIW computer processing architecture having the program counter stored in a register file register

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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